

## DM74LS245 3-STATE Octal Bus Transceiver

### General Description

These octal bus transceivers are designed for asynchronous two-way communication between data buses. The control function implementation minimizes external timing requirements.

The device allows data transmission from the A Bus to the B Bus or from the B Bus to the A Bus depending upon the logic level at the direction control (DIR) input. The enable input ( $\bar{G}$ ) can be used to disable the device so that the buses are effectively isolated.

### Features

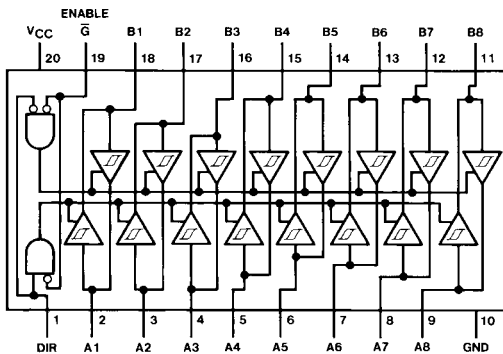
- Bi-Directional bus transceiver in a high-density 20-pin package
- 3-STATE outputs drive bus lines directly
- PNP inputs reduce DC loading on bus lines
- Hysteresis at bus inputs improve noise margins
- Typical propagation delay times, port-to-port 8 ns
- Typical enable/disable times 17 ns
- $I_{OL}$  (sink current)  
24 mA
- $I_{OH}$  (source current)  
-15 mA

### Ordering Code:

| Order Number | Package Number | Package Description   |
|--------------|----------------|---|
| DM74LS245WM  | M20B           | 20-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-013, 0.300 Wide |
| DM74LS245SJ  | M20D           | 20-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide             |
| DM74LS245N   | N20A           | 20-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide     |

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

### Connection Diagram



### Function Table

| Enable<br>$\bar{G}$ | Direction<br>Control<br>DIR | Operation       |
|---------------------|-----------------------------|-----------------|
| L                   | L                           | B Data to A Bus |
| L                   | H                           | A Data to B Bus |
| H                   | X                           | Isolation       |

H = HIGH Level  
L = LOW Level  
X = Irrelevant

**Absolute Maximum Ratings**(Note 1)

|                                      |                 |
|--------------------------------------|-----------------|
| Supply Voltage                       | 7V              |
| Input Voltage                        |                 |
| DIR or $\bar{G}$                     | 7V              |
| A or B                               | 5.5V            |
| Operating Free Air Temperature Range | 0°C to +70°C    |
| Storage Temperature Range            | -65°C to +150°C |

**Note 1:** The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

**Recommended Operating Conditions**

| Symbol          | Parameter                      | Min  | Nom | Max  | Units |
|-----------------|--------------------------------|------|-----|------|-------|
| V <sub>CC</sub> | Supply Voltage                 | 4.75 | 5   | 5.25 | V     |
| V <sub>IH</sub> | HIGH Level Input Voltage       | 2    |     |      | V     |
| V <sub>IL</sub> | LOW Level Input Voltage        |      |     | 0.8  | V     |
| I <sub>OH</sub> | HIGH Level Output Current      |      |     | -15  | mA    |
| I <sub>OL</sub> | LOW Level Output Current       |      |     | 24   | mA    |
| T <sub>A</sub>  | Free Air Operating Temperature | 0    |     | 70   | °C    |

**Electrical Characteristics**

over recommended operating free air temperature range (unless otherwise noted)

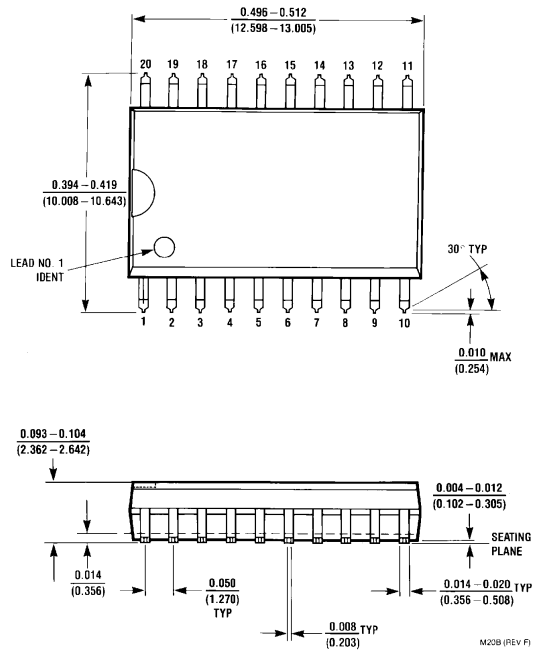
| Symbol           | Parameter  | Conditions   | Min                                     | Typ (Note 2) | Max  | Units |    |
|------------------|--|--|---|--------------|------|-------|----|
| V <sub>I</sub>   | Input Clamp Voltage                                  | V <sub>CC</sub> = Min, I <sub>I</sub> = -18 mA   |   |              | -1.5 | V     |    |
| HYS              | Hysteresis (V <sub>T+</sub> - V <sub>T-</sub> )      | V <sub>CC</sub> = Min  | 0.2                                     | 0.4          |      | V     |    |
| V <sub>OH</sub>  | HIGH Level Output Voltage                            | V <sub>CC</sub> = Min, V <sub>IH</sub> = Min<br>V <sub>IL</sub> = Max, I <sub>OH</sub> = -1 mA | 2.7                                     |              |      | V     |    |
|                  |  | V <sub>CC</sub> = Min, V <sub>IL</sub> = Min<br>V <sub>IL</sub> = Max, I <sub>OH</sub> = -3 mA | 2.4                                     | 3.4          |      |       |    |
|                  |  | V <sub>CC</sub> = Min, V <sub>IH</sub> = Min<br>V <sub>IL</sub> = 0.5V, I <sub>OH</sub> = Max  | 2                                       |              |      |       |    |
| V <sub>OL</sub>  | LOW Level Output Voltage                             | V <sub>CC</sub> = Min<br>V <sub>IL</sub> = Max<br>V <sub>IH</sub> = Min                        | I <sub>OL</sub> = 12 mA                 |              | 0.4  | V     |    |
|                  |  |  | I <sub>OL</sub> = Max                   |              | 0.5  |       |    |
| I <sub>OZH</sub> | Off-State Output Current, HIGH Level Voltage Applied | V <sub>CC</sub> = Max<br>V <sub>IL</sub> = Max   | V <sub>O</sub> = 2.7V                   |              | 20   | μA    |    |
| I <sub>OZL</sub> | Off-State Output Current, LOW Level Voltage Applied  | V <sub>IH</sub> = Min  | V <sub>O</sub> = 0.4V                   |              | -200 | μA    |    |
| I <sub>I</sub>   | Input Current at Maximum Input Voltage               | V <sub>CC</sub> = Max  | A or B<br>V <sub>I</sub> = 5.5V         |              | 0.1  | mA    |    |
|                  |  |  | DIR or $\bar{G}$<br>V <sub>I</sub> = 7V |              | 0.1  |       |    |
| I <sub>IH</sub>  | HIGH Level Input Current                             | V <sub>CC</sub> = Max, V <sub>I</sub> = 2.7V   |   |              | 20   | μA    |    |
| I <sub>IL</sub>  | LOW Level Input Current                              | V <sub>CC</sub> = Max, V <sub>I</sub> = 0.4V   |   |              | -0.2 | mA    |    |
| I <sub>OS</sub>  | Short Circuit Output Current                         | V <sub>CC</sub> = Max (Note 3)   |   | -40          | -225 | mA    |    |
| I <sub>CC</sub>  | Supply Current                                       | V <sub>CC</sub> = Max  | Outputs HIGH                            |              | 48   | 70    | mA |
|                  |  |  | Outputs LOW                             |              | 62   | 90    |    |
|                  |  |  | Outputs at Hi-Z                         |              | 64   | 95    |    |

**Note 2:** All typicals are at V<sub>CC</sub> = 5V, T<sub>A</sub> = 25°C.

**Note 3:** Not more than one output should be shorted at a time, not to exceed one second duration

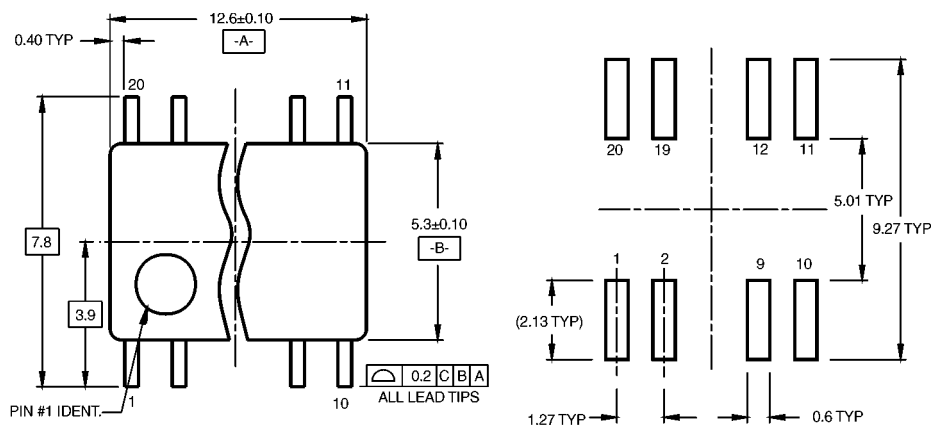
| <b>Switching Characteristics</b> |   |  |     |     |       |
|----------------------------------|---|--|-----|-----|-------|
| $V_{CC} = 5V, T_A = 25^\circ C$  |   |  |     |     |       |
| Symbol                           | Parameter   | Conditions                                 | Min | Max | Units |
| $t_{PLH}$                        | Propagation Delay Time,<br>LOW-to-HIGH Level Output | $C_L = 45\text{ pF}$<br>$R_L = 667\Omega$  |     | 12  | ns    |
| $t_{PHL}$                        | Propagation Delay Time,<br>HIGH-to-LOW Level Output |  |     | 12  | ns    |
| $t_{PZL}$                        | Output Enable Time<br>to LOW Level                  |  |     | 40  | ns    |
| $t_{PZH}$                        | Output Enable Time<br>to HIGH Level                 |  |     | 40  | ns    |
| $t_{PLZ}$                        | Output Disable Time<br>from LOW Level               | $C_L = 5\text{ pF}$<br>$R_L = 667\Omega$   |     | 25  | ns    |
| $t_{PHZ}$                        | Output Disable Time<br>from HIGH Level              |  |     | 25  | ns    |
| $t_{PLH}$                        | Propagation Delay Time,<br>LOW-to-HIGH Level Output | $C_L = 150\text{ pF}$<br>$R_L = 667\Omega$ |     | 16  | ns    |
| $t_{PHL}$                        | Propagation Delay Time,<br>HIGH-to-LOW Level Output |  |     | 17  | ns    |
| $t_{PZL}$                        | Output Enable Time<br>to LOW Level                  |  |     | 45  | ns    |
| $t_{PZH}$                        | Output Enable Time<br>to HIGH Level                 |  |     | 45  | ns    |

**Physical Dimensions** inches (millimeters) unless otherwise noted

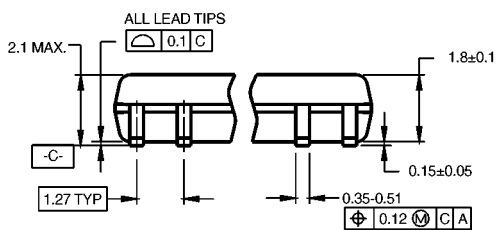


**20-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-013, 0.300 Wide  
Package Number M20B**

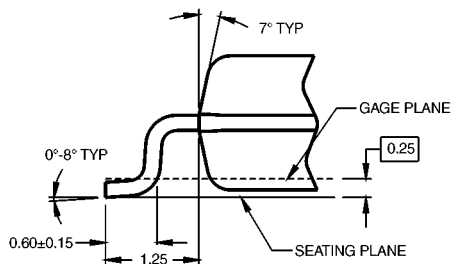
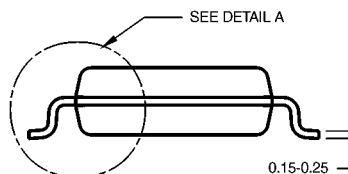
**Physical Dimensions** inches (millimeters) unless otherwise noted (Continued)



**LAND PATTERN RECOMMENDATION**



DIMENSIONS ARE IN MILLIMETERS



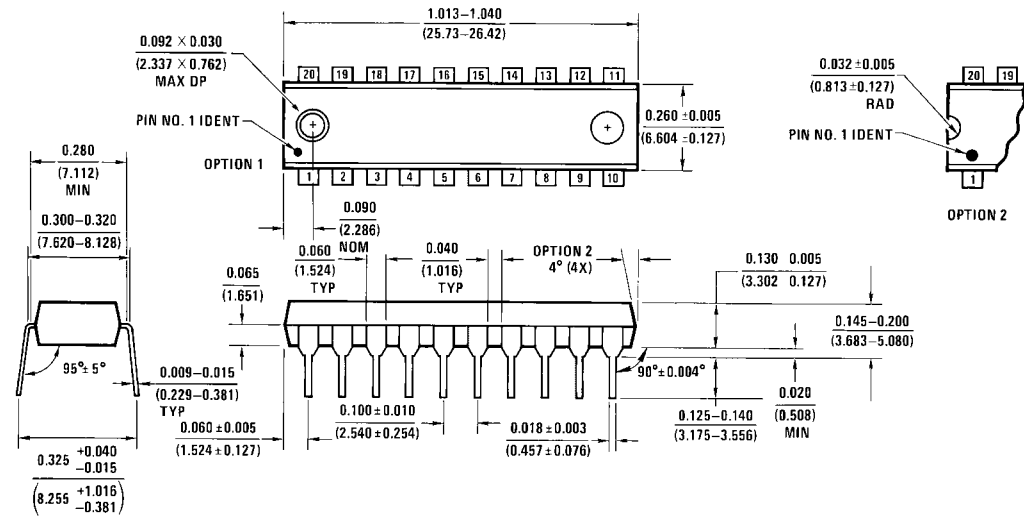
**DETAIL A**

- NOTES:
- A. CONFORMS TO EIAJ EDR-7320 REGISTRATION, ESTABLISHED IN DECEMBER, 1998.
  - B. DIMENSIONS ARE IN MILLIMETERS.
  - C. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.

M20DRevB1

**20-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide  
Package Number M20D**

**Physical Dimensions** inches (millimeters) unless otherwise noted (Continued)



**20-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide  
Package Number N20A**

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